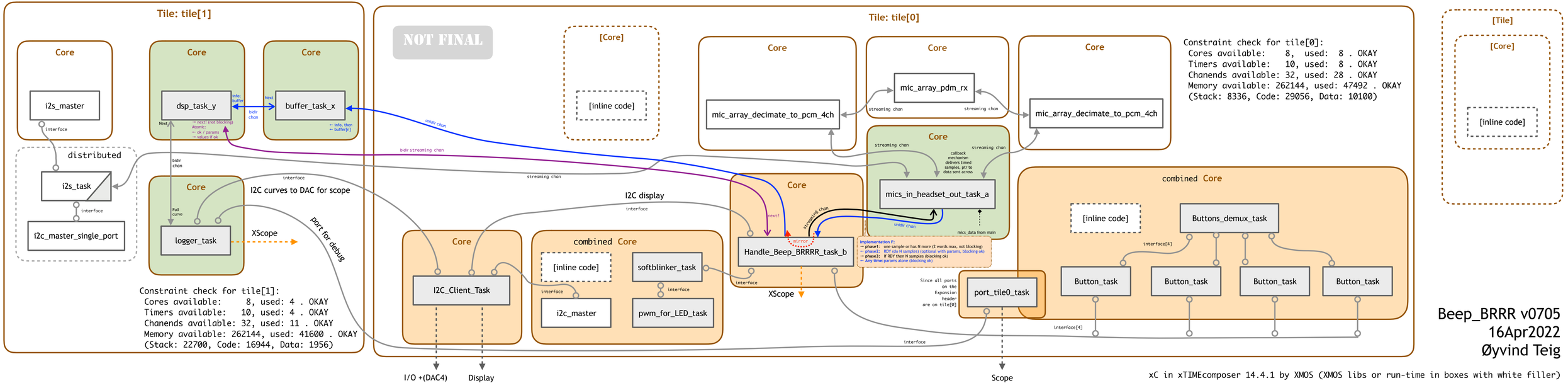
Tile: tile[1]
Core
i2s_master
interface
distributed
i2s_task
interface
i2c_master_single_port
Core
dsp_task_y
Next
info, buffer
bidir chan
→ next! (not blocking)
Atomic:
← ok / params
→ values if ok
Core
buffer_task_x
Next
← info, then
← buffer[n]
bidir chan
Core
Full curve
logger_task
interface
I2C curves to DAC for scope
XScope
port for debug
Constraint check for tile[1]:
Cores available: 8, used: 4 . OKAY
Timers available: 10, used: 4 . OKAY
Chanends available: 32, used: 11 . OKAY
Memory available: 262144, used: 41600 . OKAY
(Stack: 22700, Code: 16944, Data: 1956)
NOT FINAL
Tile: tile[0]
[Core]
[inline code]
Core
mic_array_decimate_to_pcm_4ch
Core
mic_array_pdm_rx
streaming chan
streaming chan
Core
mic_array_decimate_to_pcm_4ch
Constraint check for tile[0]:
Cores available: 8, used: 8 . OKAY
Timers available: 10, used: 8 . OKAY
Chanends available: 32, used: 28 . OKAY
Memory available: 262144, used: 47492 . OKAY
(Stack: 8336, Code: 29056, Data: 10100)
unidir chan
bidir streaming chan
streaming chan
Core
streaming chan
streaming chan
callback mechanism delivers timed samples, ptr to data sent across
mics_in_headset_out_task_a
uniadir chan
mics_data from main
Core
next!
mirror
streaming chan
Handle_Beep_BRRRR_task_b
interface
XScope
Implementation F:
→ phase1: one sample or has N more (2 words max, not blocking)
← phase2: RDY (do N samples) (optional with params, blocking ok)
→ phase3: If RDY then N samples (blocking ok)
← Any time:params alone (blocking ok)
I2C display
interface
Core
I2C_Client_Task
combined Core
[inline code]
softblinker_task
interface
interface
i2c_master
pwm_for_LED_task
Since all ports on the Expansion header are on tile[0]
port_tile0_task
combined Core
[inline code]
Buttons_demux_task
interface[4]
Button_task
Button_task
Button_task
Button_task
interface[4]
interface
I/O +(DAC4)
Display
Scope
[Tile]
[Core]
[inline code]
Beep_BRRR v0705
16Apr2022
Øyvind Teig
xC in xTIMEcomposer 14.4.1 by XMOS (XMOS libs or run-time in boxes with white filler)